BITS, PILANI – DUBAI, ACADEMIC CITY, DUBAI FIRST SEMESTER 2009 – 2010 ES C241 ELECTRICAL SCIENCES – I

COMPREHENSIVE EXAMINATION (CLOSED BOOK)

MAXIMUM MARKS: 120 WEIGHTAGE: 40% DATE: 22/10/09 DURATION: 3 HOURS

Write PART A & B & C in separate answer sheets Answer all questions

PART A

1. Consider the series parallel circuit shown in Figure 1

[12 Marks]

- a) Find V_s when v₁=2V
- b) Find V_s when i₃=3A
- c) Find V_s when i₅=4A
- d) Find the Power dissipated in 5Ω resistance when v₁=4V

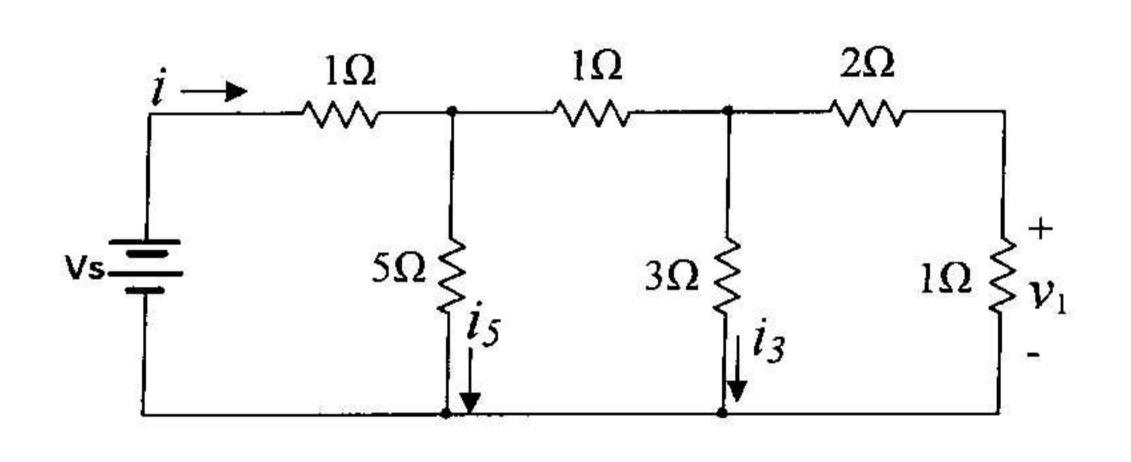


Figure 1

2. Find the node voltages for the circuit shown in Figure 2.

[12 marks]

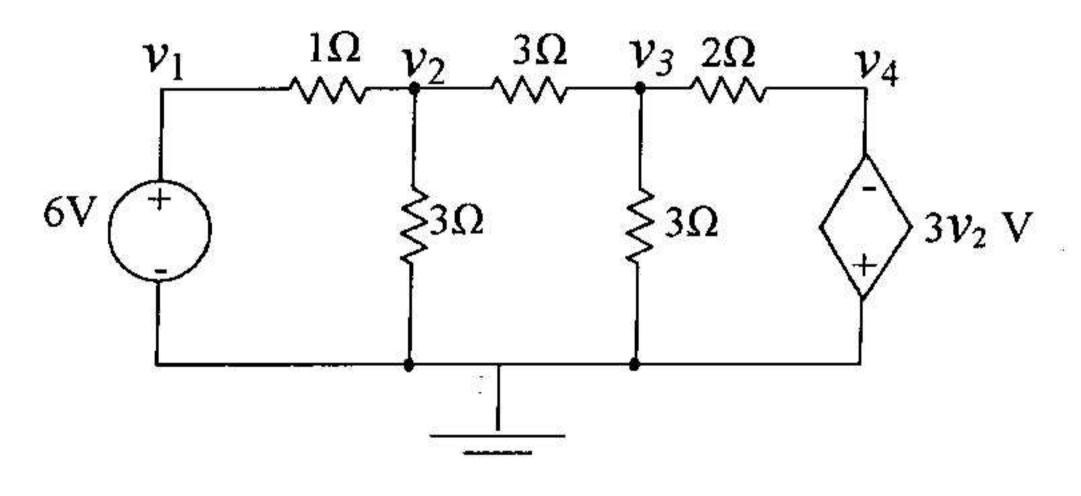


Figure 2

3. For the circuit shown in Figure 3 a) Find the Thevenin equivalent of the resulting circuit to the left of terminals a and b. b) Use the Thevenins equivalent circuit to find the power absorbed by $R_L = 2\Omega$. c) Determine the value of R_L which absorbs the maximum amount of power and find this power. [12 Marks]

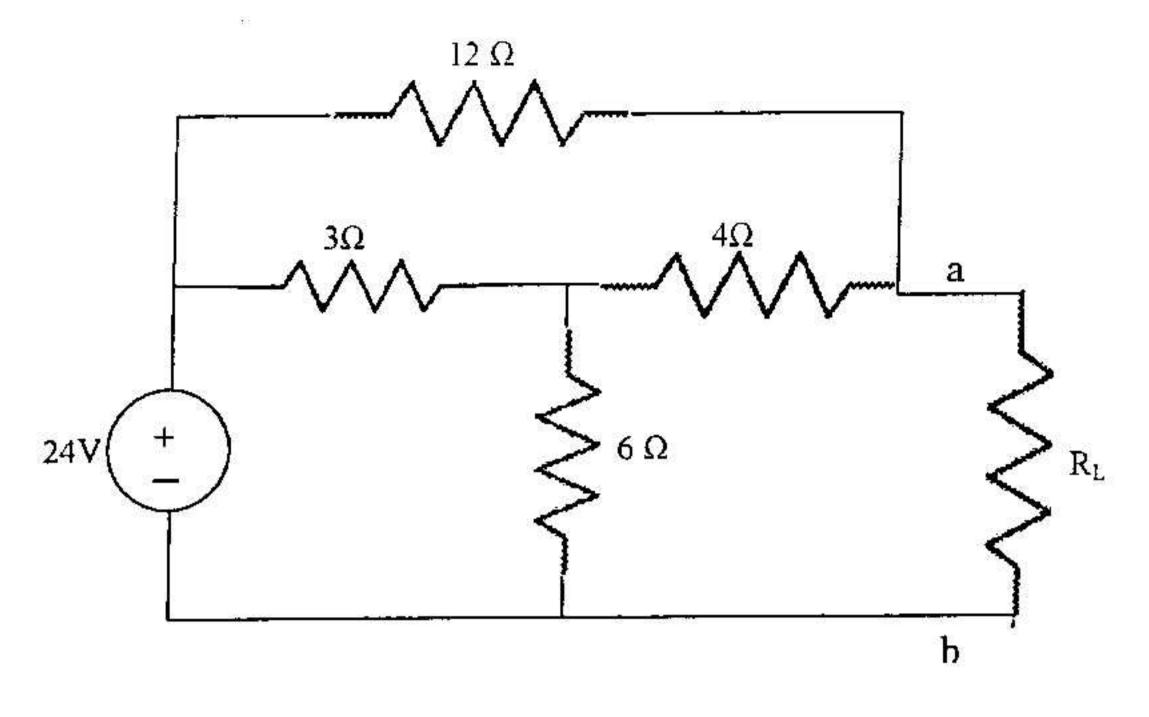


Figure 3

4. Draw the circuit diagram of OPAMP differential amplifier and obtain the expression for the output voltage V_{\circ}

PART B

5. For the circuit shown in Figure 4 find V_c and V_R at time t<0, t>0, t=1.5 m sec.

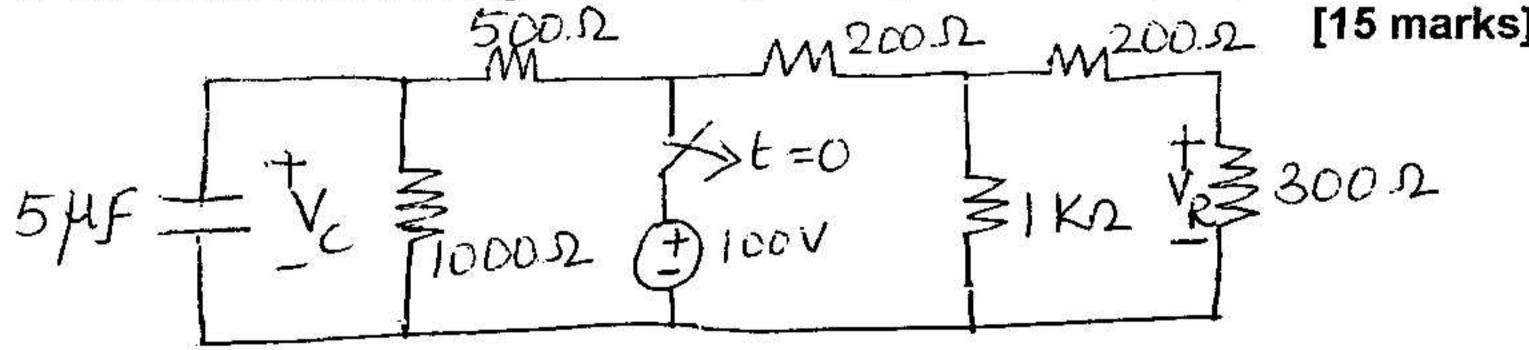


Figure 4

6. For the circuit shown in Figure 5 find the complete response for the current flow through the inductor, i_L (0-)and i_L(0+) and i_L(t) for all values of t. [9 marks]

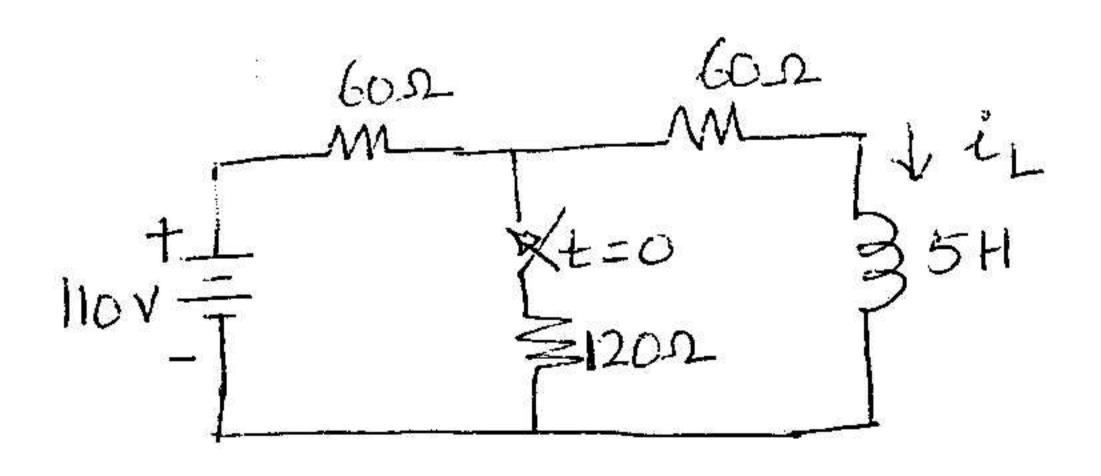
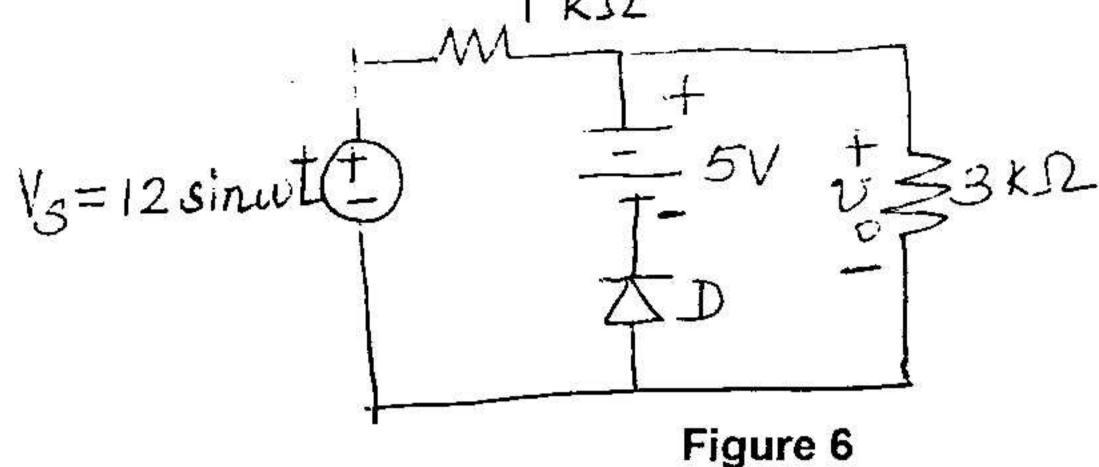


Figure 5

7. For the ideal diode circuit given in Figure 6, determine the output voltage V_o and Sketch this function. [12 Marks]



- 8. The Zener diode in the voltage regulator circuit shown in Figure 7 has a breakdown voltage of 9V and is to operate with a reverse current between 10mA and 100mA. Given that $R=200\Omega$, Find
 - (a) Range of load resistance (R_L) that results in a 9V load voltage when V_s =24V.
 - (b) Range of Supply voltage (V_s) that results in a 9 V load voltage when R_L =600 Ω .

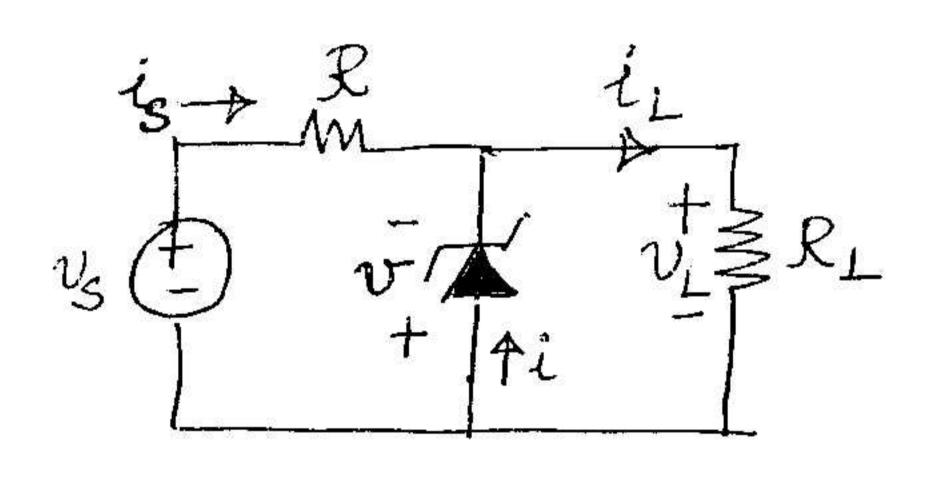


Figure 7

PART C

9. For the circuit given in Figure 8, suppose that $R_c=3~k\Omega$, $R_B=50~k\Omega$, $V_{BB}=V_{CC}=5V$ and $\beta=100$. Assume that the BJT operates in the active region and find i_B , i_C and v_{CE} . Comment about the assumption made. [8 marks]

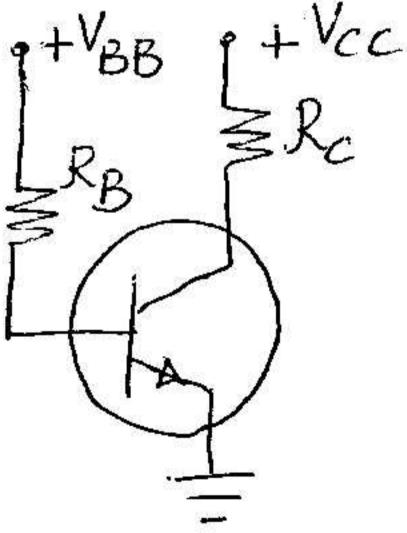
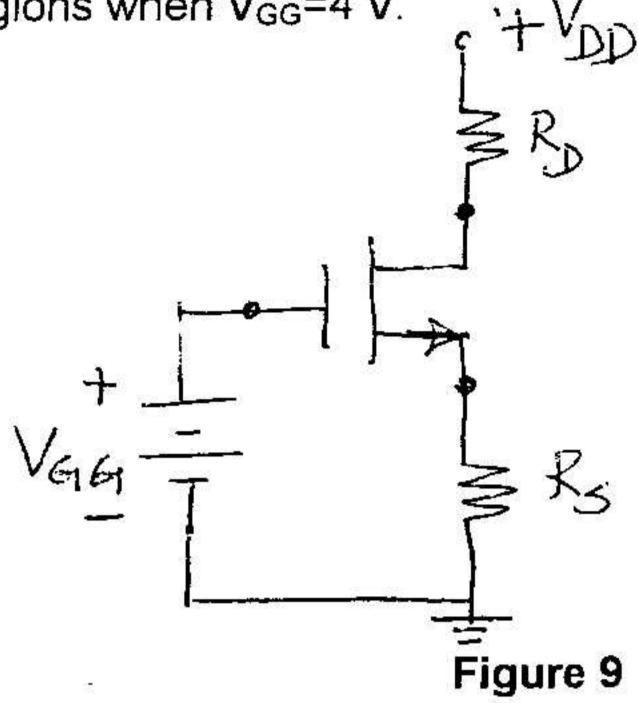


Figure 8

10. For the circuit shown in Figure 9, the enhancement MOSFET has K=0.25 mA/V² and V_{t} =2 V. Given that R_{s} =0 Ω and V_{DD} = 12 V, determine the value of R_{D} for which the MOSFET will operate on the border between the active and ohmic regions when V_{GG} =4 V. [8 marks]



11. Use De Morgan's theorem to obtain a NOR-gate realisation of the expression

$$Y = (A+B+C)(A+B+\overline{C})(A+\overline{B}+C)(A+\overline{B}+C)(\overline{A}+B+C)$$
 [4 marks]

12. Draw the drain characteristics of a p-channel JFET, showing typical values.

[4 marks]

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MAXIMUM MARKS: 60 DATE: 15/10/09 WEIGHTAGE: 20%
DURATION: 50 MINUTES

For the circuit shown in Figure 1, suppose that i_s(t) = 10 A for t < 0 s and i_s(t) = 0 A for t >= 0 s. Determine (i) the capacitor voltage v(t) for all time t and sketch this function. (ii) Hence find current through the capacitor i(t) for all time t and sketch this function.

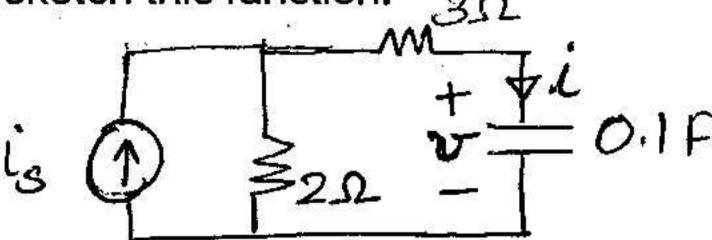


Figure 1

The input voltage to a clipper circuit shown in the figure below is V_s = 4 Sin ωt V. Determine the output voltage V₀ and sketch this function.[12 marks]

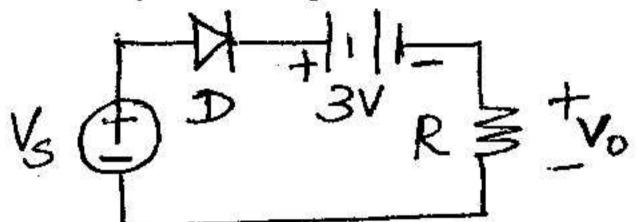


Figure 2

 For the circuit shown in Figure3, find the complete response of the current flow through the inductor for all values of t.

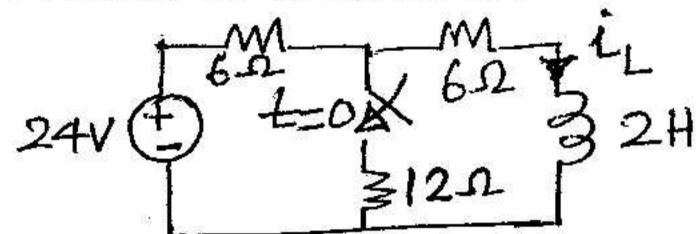


Figure 3

4. The zener diode in the voltage regulator circuit given in Figure no. 4 has a breakdown voltage of 9V. Suppose that a diode has a resistance of 100Ω in the breakdown state and the diode is to operate with a reverse current between 10 and 100mA.

[12 marks]

Find the range of v_s for the case that R=200 Ω and RL=6 Ω

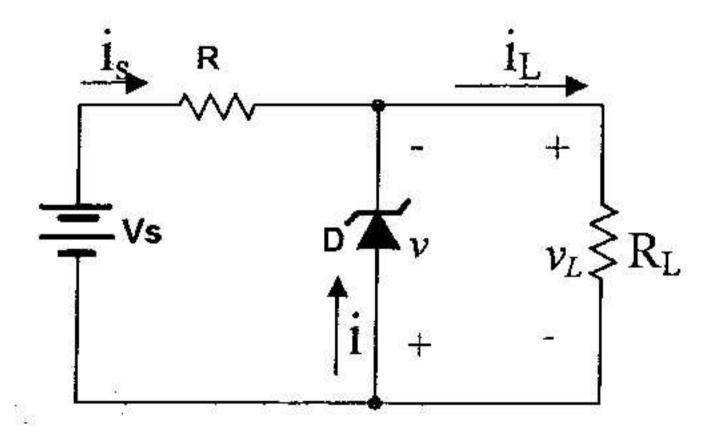


Figure 4

5. Consider a series RLC circuit with L = 1.7 H, R = 2800 Ω , C = 0.001 μ F, i(0)= 2mA and Capacitor voltage $V_c(0)$ = 2V. Sketch i(t) [12 marks]

BITS, PILANI – DUBAI, ACADEMIC CITY, DUBAI FIRST SEMESTER 2009 – 2010 ES C241 ELECTRICAL SCIENCES – I TEST 1(CLOSED BOOK)

MAXIMUM MARKS: 25

DATE: 27/09/09

WEIGHTAGE: 25% DURATION: 50 MINUTES

1. Consider the circuit shown in Figure 1. Use the current divider formula to find it is is is and if it. 4.0. It is a continuous formula to find

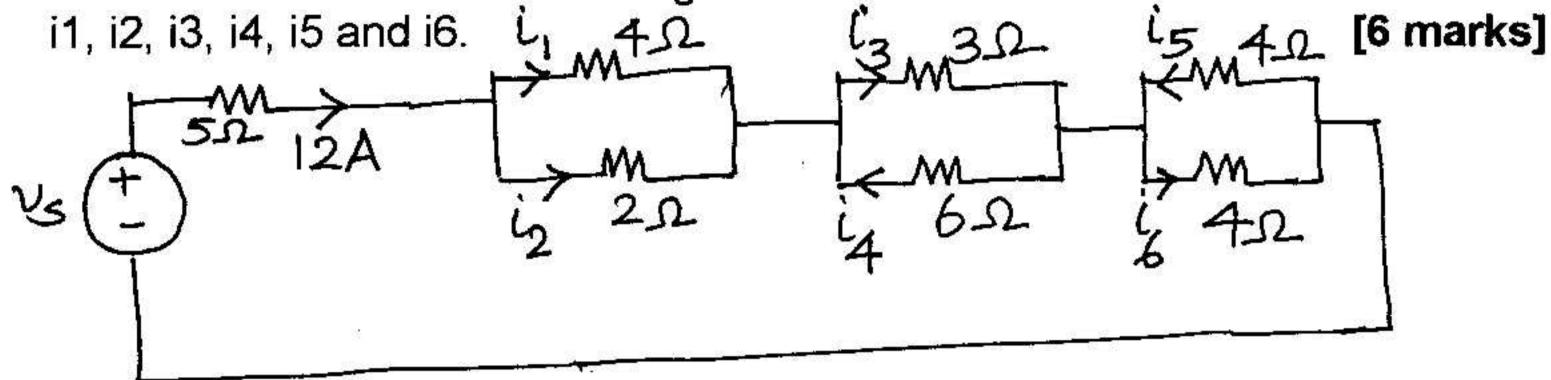


Figure 1

2. For the circuit shown in Figure 2, find current I.

[5 marks]

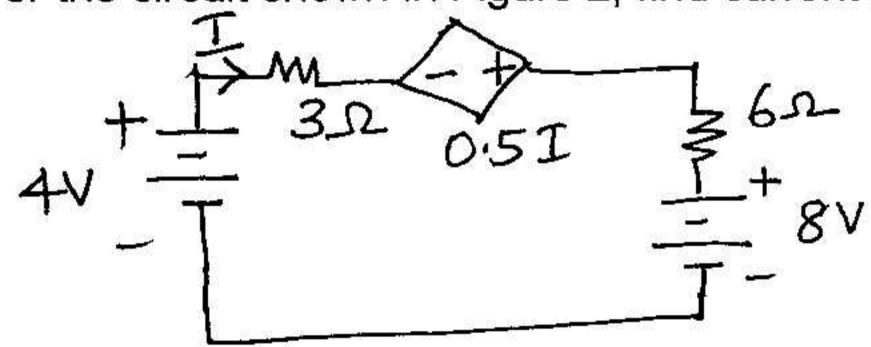


Figure 2

3. For the circuit shown in Figure3, using nodal analysis find the voltages V1, V2 and V3. [7 marks]

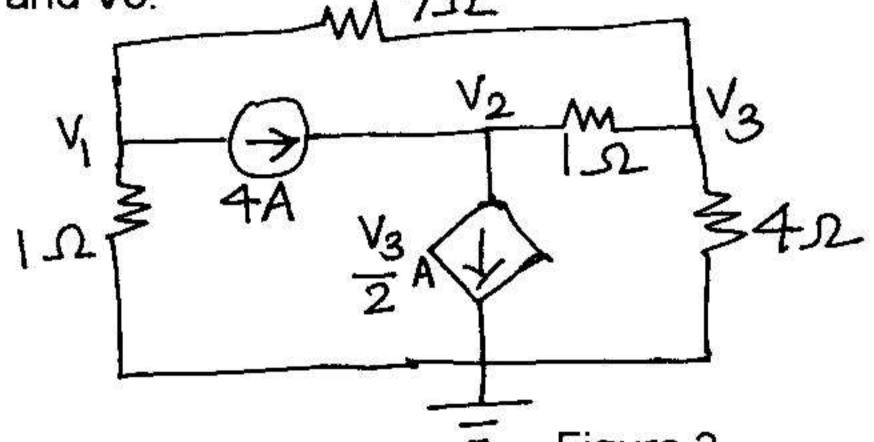


Figure 3

4. For the circuit shown in Figure 4, using mesh analysis find the mesh currents I1,I2, and I3. $+ \sqrt{2}$

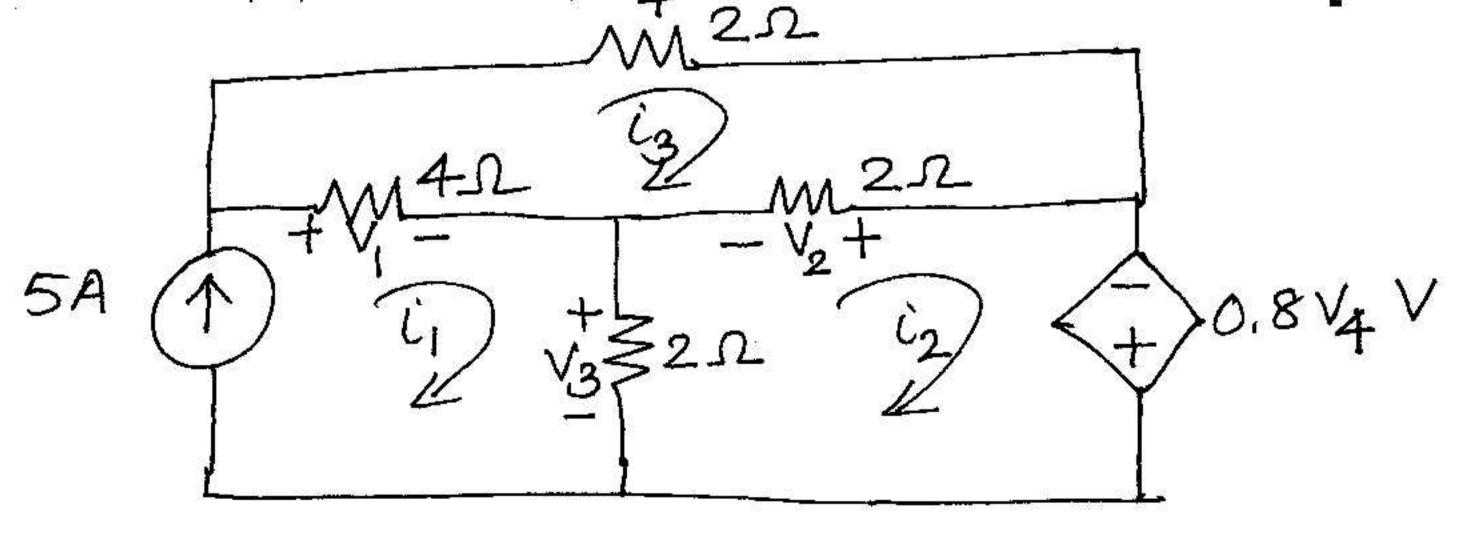


Figure 4

BITS, PILANI – INTERNATIONAL ACADEMIC CITY, DUBAI FIRST SEMESTER 2009 – 2010 ES UC 241 ELECTRICAL SCIENCES – I QUIZ 2 (CLOSED BOOK)

MAXIMUM MARK DATE: 15.1		VEIGHTAGE: 7 % ON: 25 MINUTES
NAME:	Id. No.:	
 What kind of a d Bilateral Linear Nonlinear Unipolar 	device is a diode?	[1 Mark]
 2. Which of these is a) It is a rectifier dis b) It is a constant ver c) It is a constant con	oltage device arrent device	[1 Mark]
3. For a silicon did current at temperatur Ans:	ode the saturation current at 300K is 10 are 316 K.	nA. Find the saturation [2 marks]
	reuit shown $V_s = 2$ volts and the silicon dio at 300 K. Given that $v = 0.7$ volts, $\eta = 2$,	
$ \begin{array}{c c} i_1 & R1 \\ + \nu_1 - \\ \hline - & \end{array} $	$\left\{\begin{array}{c} + \\ v \end{array}\right\}$ $\left\{\begin{array}{c} R2 \\ \downarrow i_2 \end{array}\right\}$	
egion. Its collector curr	i ₁ = i ₂ = with Common Emitter configuration, is extent is 4.9 mA, and the emitter current is and b	
- 1988년 1988년 1일 - 1988년 - 1988년 - 1988년 - 1988년 1988년 - 1988년 1988년 1988년 1988년 1988년 - 1988년 - 1988년 1 - 1988년 1988년 1988년 - 1988년 - 1988년 1988년 - 1988년 1988년 1988년 1988년 1988년 1988년 1988년 1988년 1988년 - 1988년	ith Common Base configuration, is opera and Collector-Base junction reverse biase	

reverse current Ico, a reasonable approximation relating the collector current to the

[2 marks]

emitter current is ______

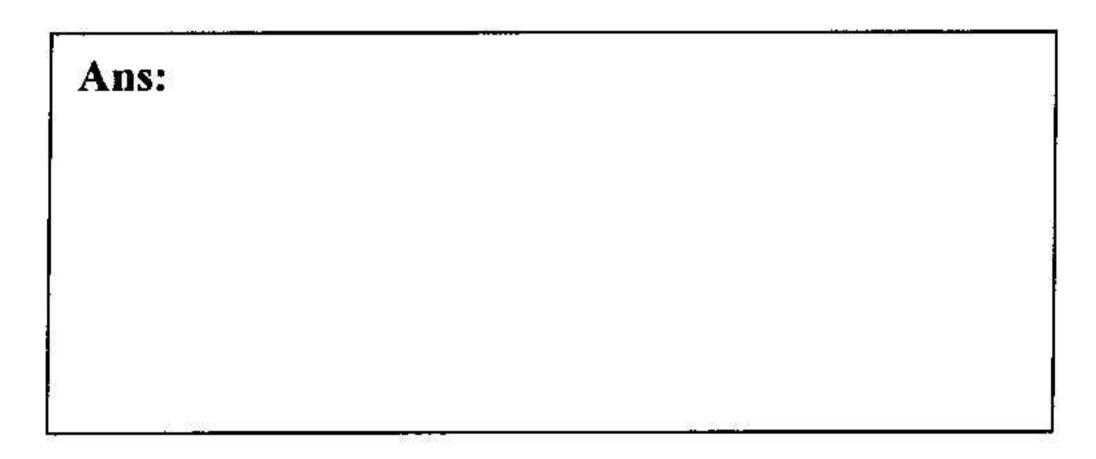
7)	In	an	npn	transis	stor	with	Comm	on	Emitte	r cont	igur	ation,	the	ez	xcess	sive	cur	rent
res	ulti	ng	fr	om	tra	nsisto	r bi	reak	down	can	1	be	du	ıe	t	0	ei	ther
			0710					or	to	anothe	er	pheno	omer	101	n l	knov	vn	as
			DES.		60 88366		RAT			80-74						[2	mar	ks]

(8) Which of the following relation is correct in the case of BJT

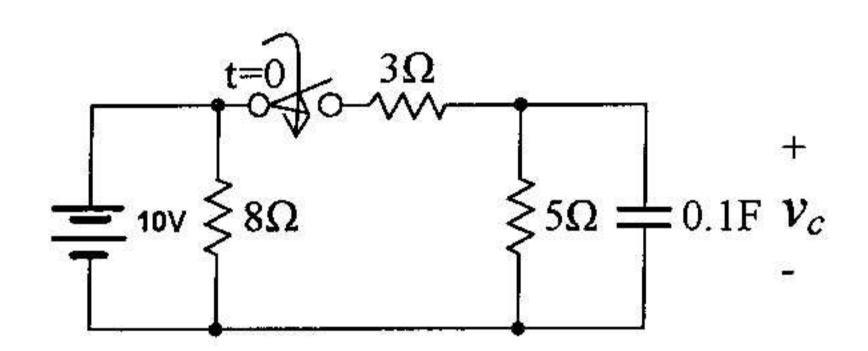
[1 mark]

- (a) $\alpha = \beta/(1-\beta)$
- (b) $\alpha = \beta/(1+\beta)$
- (c) $\beta = (\alpha+1)/\alpha$
- (d) $\beta = (\alpha 1)/\alpha$
- (9) Draw the circuit diagram for zener voltage regulator

[2 marks]

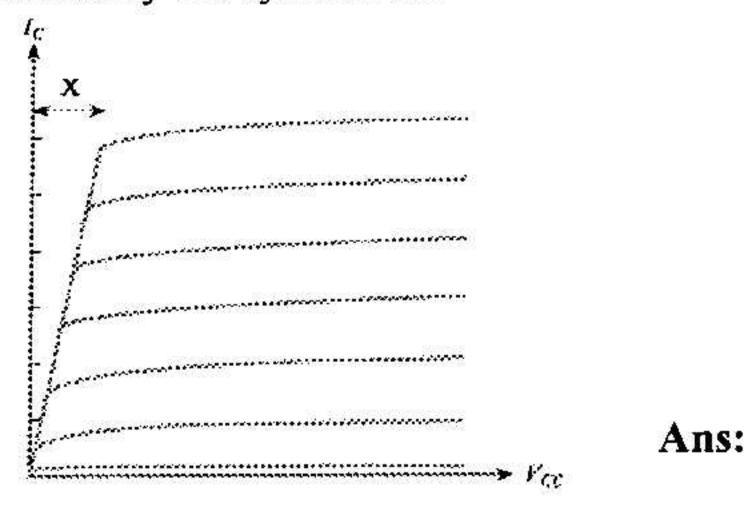


10. For the circuit shown below find the voltage across the capacitor Vc at steady state ($t = \infty$).



Ans:....

11. In the bipolar transistor output characteristics shown below, what region is represented by the symbol 'x'? [1 Mark]



Rough Work

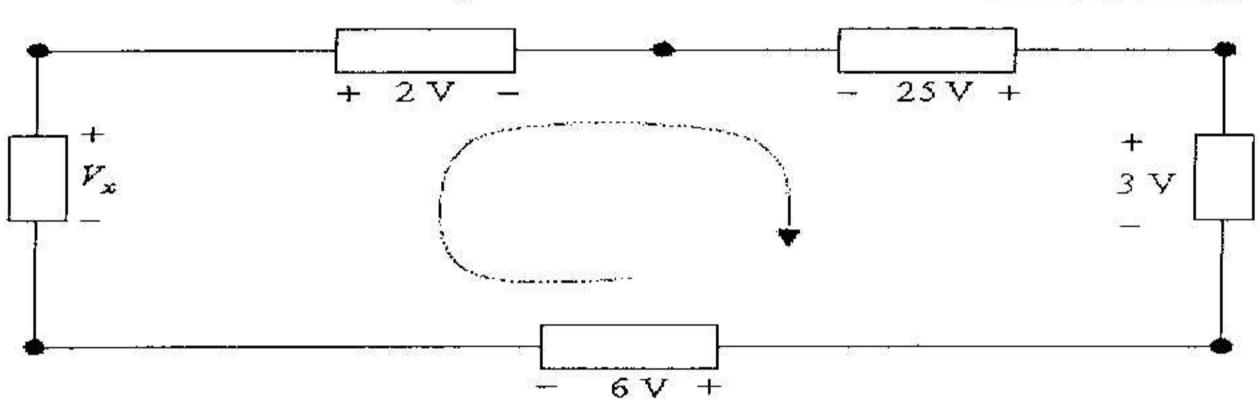
BITS, PILANI – INTERNATIONAL ACADEMIC CITY, DUBAI FIRST SEMESTER 2009 – 2010 ES UC 241 ELECTRICAL SCIENCES – I QUIZ 1 (CLOSED BOOK)

MAXIMUM MARKS: 24 DATE: 06.10.09

WEIGHTAGE: 8 % DURATION: 25 MINUTES

NAME: Id. No.:

- 1. Two resistors R1 and R2 are connected in series to a voltage source Vs. The voltage across the first resistor is [2M]
- 2. Find the unknown voltage in the circuit shown below ______V [2M]



3. Identify the incorrect statements with respect to the Thevenin theorem

[1M]

- I. Remove the load to find open circuit voltage
- II. Short circuit the load to find the open circuit voltage
- III. Open circuit all voltage sources to find Thevenin's equivalent resistance R₀
- IV. Short all the voltage sources to find Thevenin's equivalent resistance R₀
 - A. III, IV
 - B. II, III
 - C. I,II
 - D. All are incorrect
- 4. If a resistor has 5.5 V across it and 3 mA flowing through it, what is the power w [1M]

5. A certain series circuit has a 100Ω , a 270Ω and 330Ω resistors in series if the 270Ω resistor is short circuited from the circuit, the current [1M] (a) Increases (b) decreases (c) exactly doubles (d) becomes zero.
6. If one of the resistors in a parallel circuit is removed, what happens to the total resistance
[1M]
7. Six light bulbs are connected in parallel across 110V, each bulb is rated 75W, Find the current flowing through each bulb [2M]
8. For the circuit shown below find out Norton equivalent resistance R_N and the Current Source value [2M]
9. In a series circuit, with unequal resistances A) the lowest resistance has the highest voltage drop B) the highest resistance has the highest voltage drop C) the lowest resistance has the highest current through it D) the highest resistance has the most of the current through it
10. A 90A current flows into two parallel resistors having resistances of 12 Ω and 24 Ω . The current in the 24 Ω resistor isA [1M]
11 Refer to the following figure, thevenin's equivalent resistance is The value of R _L that absorbs maximum power is Pmax= [2+1+1M]
12V + 62
12. An ideal voltage source is described by a function $v(t) = 5 \sin(\prod/2)t$. The value of this voltage source when $t=1$ sec isV
[1M]
13. For the circuit shown below suppose i1 is 2A. The value of v when i2 is 3A is [2M]
60 $\frac{1}{4}$ 0 $\frac{1}{4}$ 0 0 0 0 0 0 0 0 0 0

14. For the circuit shown below the values for the current (T) A, Equivalent Resistance and voltage v [3M]

