

Write PART A & B & C in separate answer sheets  
Answer all questions  
PART A

1. For the circuit shown in Figure 1 determine the value of resistance  $R$  and the currents  $i_1$  and  $i_2$  when the total current taken by the circuit is 6A. [10 marks]

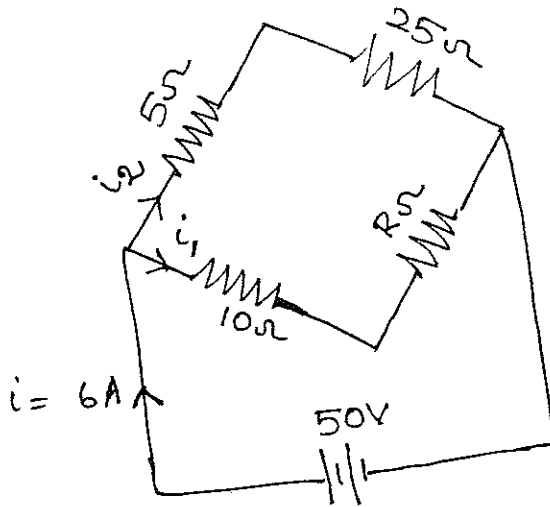


Figure 1

2. Find the node voltages for the circuit shown in Figure 2. [10 marks]

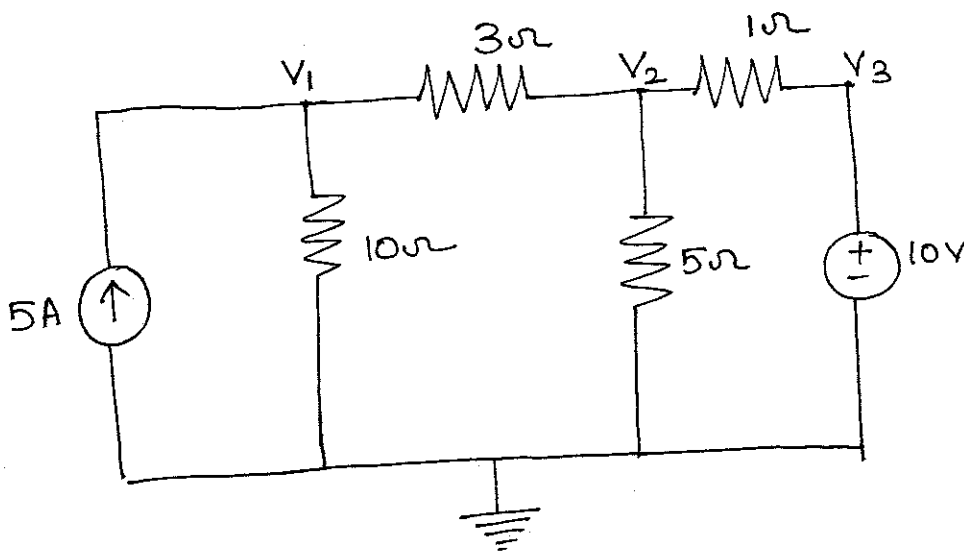


Figure 2

(P.T.O)

3. For the circuit shown in Figure 3, a) Find the Thevenin equivalent of the resulting circuit to the left of terminals a and b. b) Use the Thevenin equivalent circuit to find the power absorbed by the load resistance,  $R_L = 3\Omega$ . c) Determine the value of  $R_L$  which absorbs the maximum amount of power and find this power. [20 Marks]

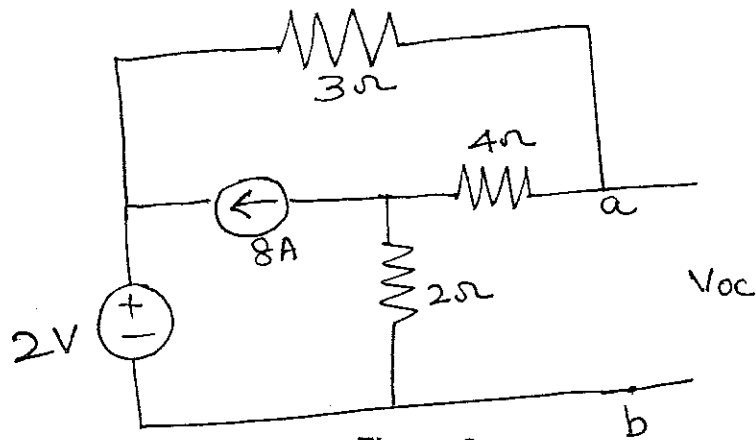


Figure 3

**PART B**

4. For the circuit shown in Figure 4 find  $V_L$  and  $i_L$  at time  $t < 0$ ,  $t > 0$ ,  $t = 0.1$  m sec.

[10 marks]

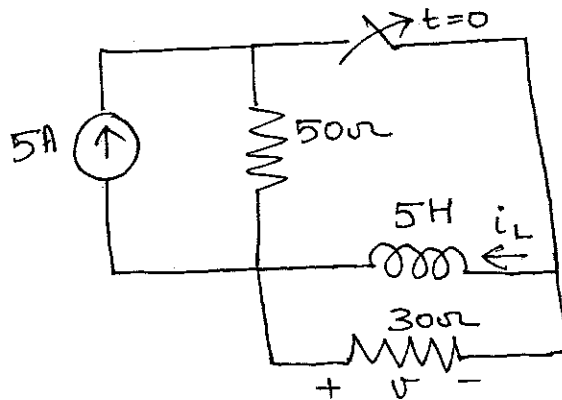


Figure 4

5. Given a series RLC circuit  $R_L = 5\Omega$ ,  $L = 0.5$  H and  $C = 0.125$  F,  $i(0) = 1$  A and  $V_C(0) = 2$  V. Find the expression and sketch the function for  $i(t)$

[20 marks]

6. For the circuit shown in Figure 5, derive the output voltage  $V_o$  in terms of input voltages.

[10 Marks]

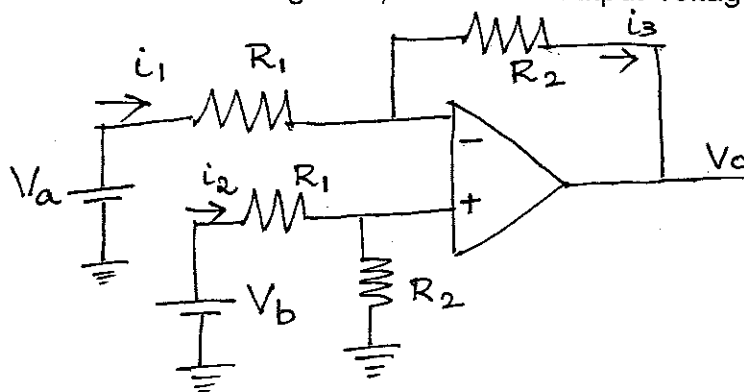
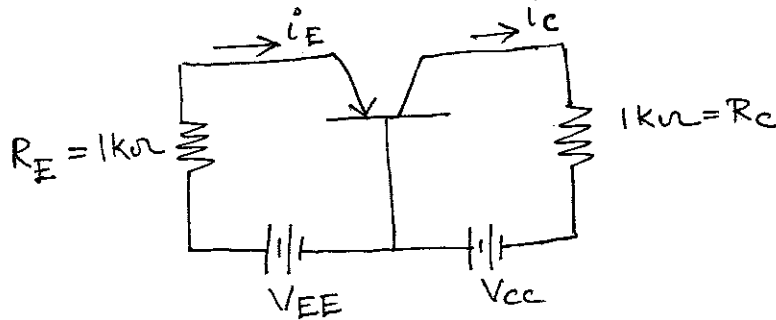


Figure 5

**PART C**

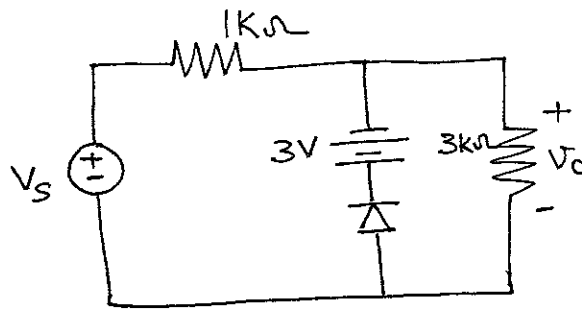
7. For the circuit shown in Figure 6 assume that the transistor is in the active region and that  $\alpha = 1$

- a) find  $V_{EE}$  such that  $i_E = 5.3 \text{ mA}$
- b) Given that  $i_E = 5.3 \text{ mA}$ , find  $V_{CC}$  such that  $V_{CB} = -3.7 \text{ V}$ .
- c) Given that  $i_E = 5.3 \text{ mA}$ , find the minimum value of  $V_{CC}$  such that the transistor is in the active region. **[15 Marks]**



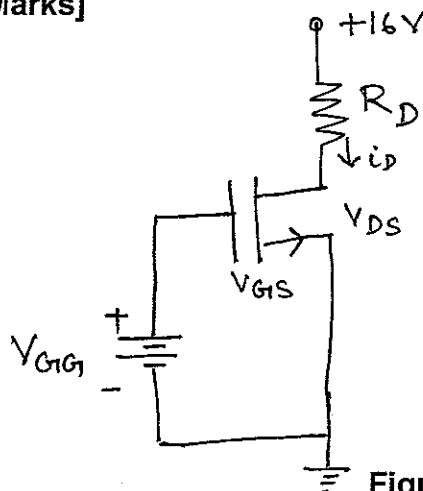
**Figure 6**

8. The input voltage to the ideal diode circuit shown in Figure 7 is  $V_s = 12 \sin \omega t \text{ V}$ . Determine the output voltage  $V_o$  and Sketch this function. **[15 Marks]**



**Figure 7**

9. For the circuit shown in Figure 8 Suppose that  $R_D = 250 \Omega$  and the Enhancement MOSFET has parameters  $k = 0.25 \text{ mA/V}^2$  and  $V_t = 2 \text{ V}$  find  $V_{GG}$  such that  $i_D = 4 \text{ mA}$  and confirm its operation in the active region. **[10 Marks]**



**Figure 8**

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 ES C241 ELECTRICAL SCIENCES – I  
 TEST 2(OPEN BOOK)

MAXIMUM MARKS: 60  
 DATE: 11/11/10

WEIGHTAGE: 20%  
 DURATION: 50 MINUTES

1. In the circuit of the following Figure 1,  $C=14.28 \text{ mF}$ ,  $R= 45 \Omega$  and  $L= 5 \text{ H}$ . the capacitor is charged to  $50 \text{ V}$ . The switch is closed at  $t=0$  seconds. List all conditions on voltages across the capacitor and current in the inductor at  $t < 0$  sec and at  $t > 0$  sec. Obtain the general solution for the current  $i(t)$  and what is the current in the circuit at  $t=0.5$  seconds. **[20 marks]**

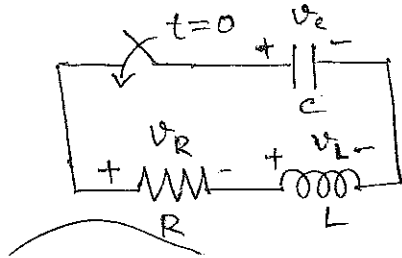


Figure 1

2. For the Op-amp circuit shown in Figure 2 write KCL equations at nodes  $V$  &  $V_1$  to find a)  $V_0$  and the resistance  $V_s/i_s$ . **[20 marks]**

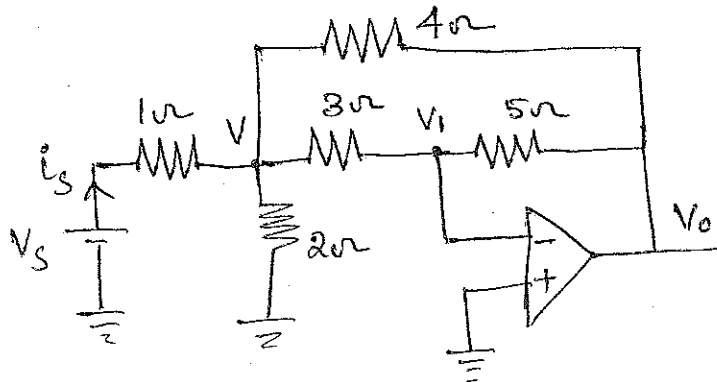


Figure 2

3. For the circuit shown in Figure3, the switch opens at  $t= 0$  sec. Find  $V_1(t)$ ,  $V_2(t)$ ,  $i_1(t)$  and  $i_2(t)$  for all values of  $t$ . **[20 marks]**

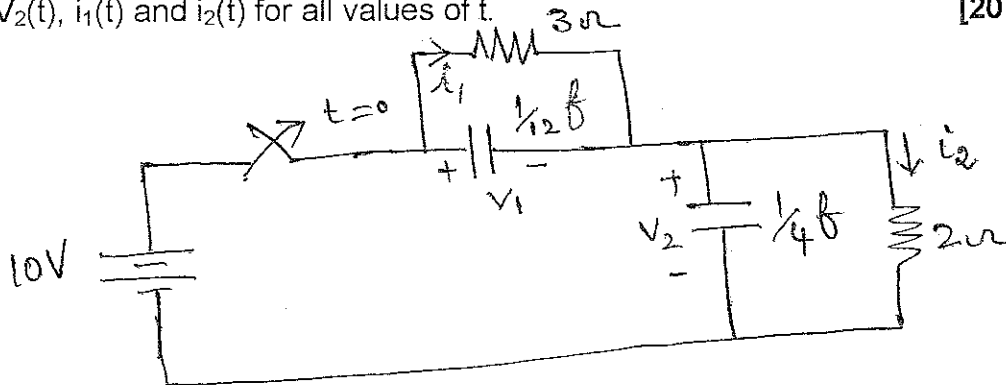


Figure 3

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 ES C241 ELECTRICAL SCIENCES – I  
 TEST 1(CLOSED BOOK)

MAXIMUM MARKS: 75  
 DATE: 03/10/10

WEIGHTAGE: 25%  
 DURATION: 50 MINUTES

1. Consider the circuit shown in Figure 1 using nodal analysis find the voltages  $V_1$ ,  $V_2$ ,  $V_3$  and  $V_4$ . [25 marks]

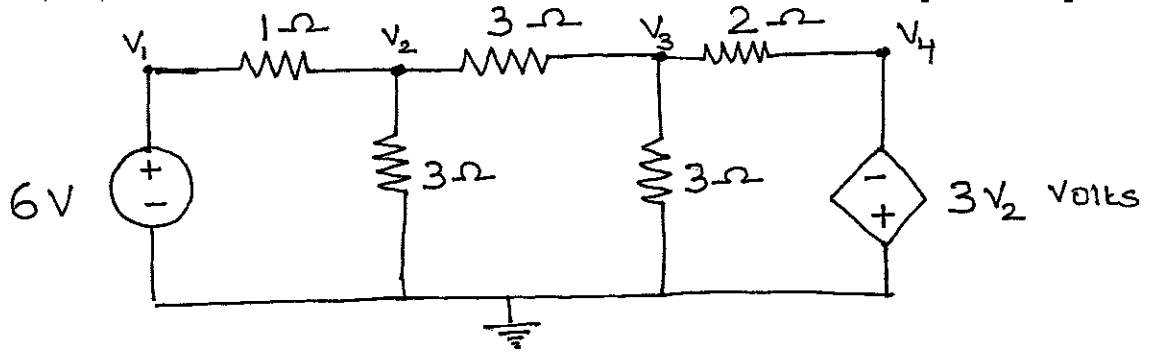


Figure 1

2. For the circuit shown in Figure 2, using Thevenin's theorem find the load current  $I_L$  in the  $10\ \Omega$  resistance. [25 marks]

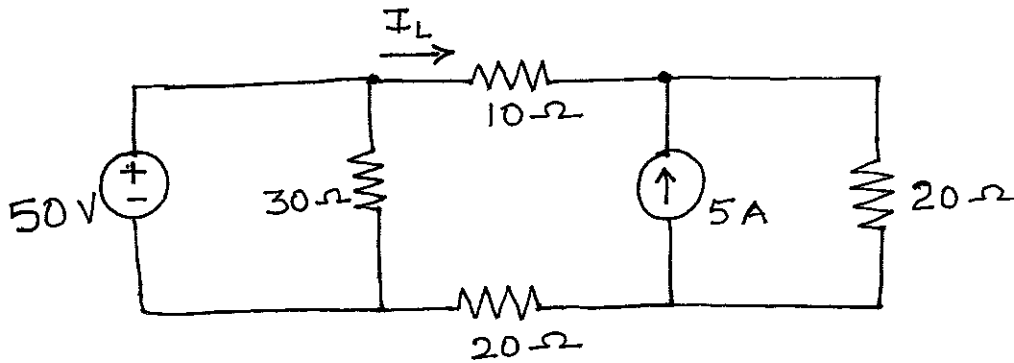


Figure 2

3. For the circuit shown in Figure 3, using mesh analysis, find the currents  $i_1$ ,  $i_2$  and  $i_3$ . [25 marks]

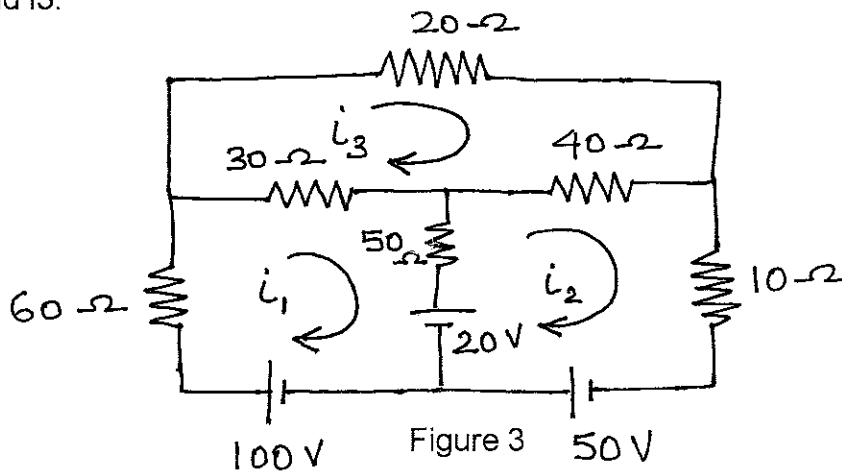


Figure 3

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**FIRST SEMESTER 2010 – 2011**  
**ES C 241 ELECTRICAL SCIENCES – I**  
**QUIZ 2 (CLOSED BOOK)**

**MAXIMUM MARKS: 21**  
**DATE: 07.12.10**

**SET 1**

**WEIGHTAGE: 7 %**  
**DURATION: 20 MINUTES**

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**NAME:**

**Id. No.:**

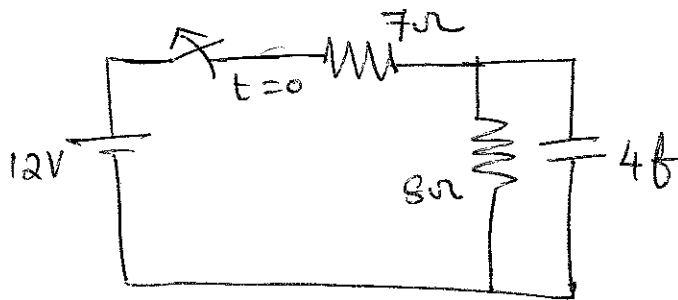
1. Draw the V- I characteristic of an ideal diode. [2 M]
2. Write the unit for the following
- (a) J current density \_\_\_\_\_
  - (b)  $\mu$  mobility of electrons \_\_\_\_\_
  - (c) E electric field strength \_\_\_\_\_
  - (d)  $\sigma$ , conductivity of metals \_\_\_\_\_ [4 M]
3. The main use of a Zener diode is in
- (a) Motor Speed Control
  - (b) Digital Electronics
  - (c) Voltage Regulation
  - (d) None of the above [1 M]
4. The intersection of diode characteristic and load line is termed as \_\_\_\_\_ [1 M]
5. What separates the two terminals inside a capacitor?
- (a) Metal
  - (b) Non Conductive material
  - (c) Water
  - (d) Non of the above [1 M]

6. Which of these elements forms a p-type semiconductor when doped with Si?

- (a) Antimony
- (b) Boron
- (c) Arsenic
- (d) Mercury

[1 M]

7. If  $V(t) = 12\text{ V}$ , what will the voltage across capacitor for  $t > 0$  seconds [3 M]



8. The time constant  $\tau$  in RL and RC circuits are given by \_\_\_\_\_ and \_\_\_\_\_ respectively [2 M]

9. Find the saturation current of the silicon diode at 360K given that the reverse saturation current at 300K is 50 nA. [2 M]

10. At 0K, pure semiconductors behave as

- (a) Insulators
- (b) Metals
- (c) Conductors
- (d) None of these

[1 M]

11. The current voltage relationship equation for a non ideal diode is given by \_\_\_\_\_ [1 M]

12. For the series RL circuit, the step responses  $i_L(t)$  is \_\_\_\_\_ and  $v_L(t)$  is \_\_\_\_\_ [2 M]

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 FIRST SEMESTER 2010 – 2011  
 ES C 241 ELECTRICAL SCIENCES – I  
 QUIZ 1 (CLOSED BOOK)

MAXIMUM MARKS: 24  
 DATE: 26.10.10

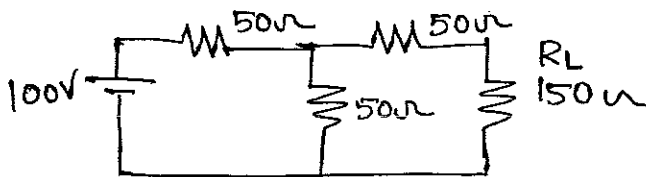
SET 1

WEIGHTAGE: 8 %  
 DURATION: 25 MINUTES

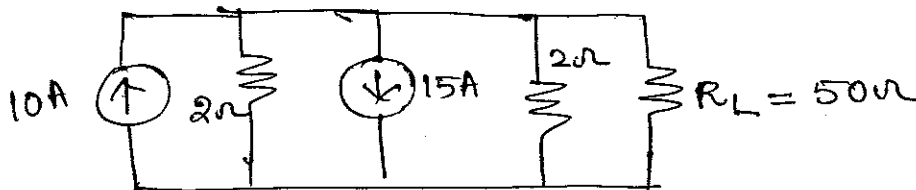
NAME:

Id. No.:

1. Refer to the following figure, thevenin's equivalent resistance is \_\_\_\_\_. The value of  $R_L$  that absorbs maximum power is \_\_\_\_\_  $P_{max} =$  \_\_\_\_\_ [1+1+1M]



2. For the circuit shown below find out Norton equivalent resistance  $R_N$  \_\_\_\_\_ and the Current Source value \_\_\_\_\_ [2M]



3. An ideal voltage source is described by a function  $v(t) = 5 \sin(\pi/2)t$ . The value of this voltage source when  $t = 1$  sec is \_\_\_\_\_ V [2M]

4. Identify the incorrect statements with respect to the Thevenin theorem [2M]

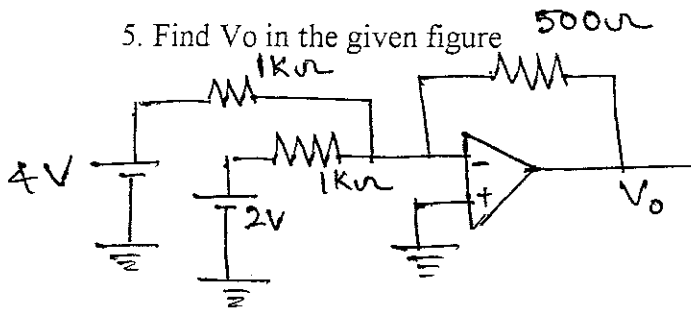
- I. Remove the load to find open circuit voltage
- II. Short circuit the load to find the open circuit voltage
- III. Open circuit all voltage sources to find Thevenin's equivalent resistance  $R_0$
- IV. Short all the voltage sources to find Thevenin's equivalent resistance  $R_0$

- A. III, IV
- B. II, III
- C. I, II
- D. All are incorrect



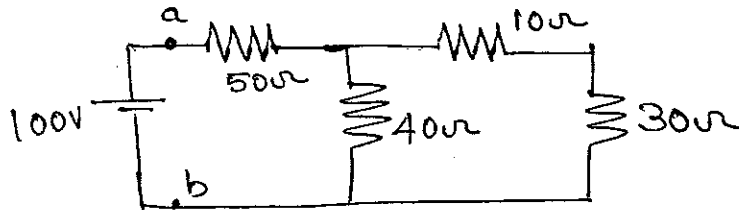
5. Find  $V_o$  in the given figure

[2 M]



6. The resistance seen at terminal a and b

[2M]



7. A closed switch has a resistance of

[1M]

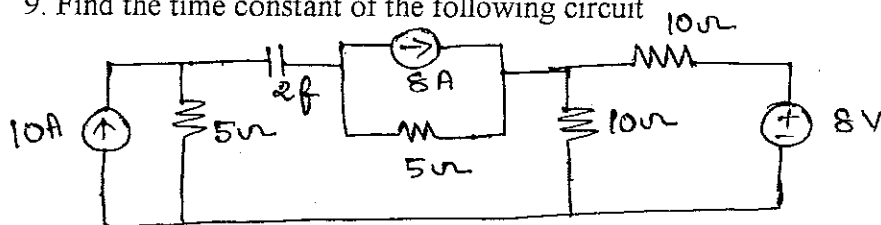
- (a) Zero
- (b) infinite
- (c) above  $10 \Omega$
- (d) at least  $1 \text{ k}\Omega$

8. Calculate the energy absorbed by a load when it draws 10 mA current at a voltage of 2 V for 30 minutes

[2M]

9. Find the time constant of the following circuit

[4M]



10. Find  $i(t)$  for all values of  $t$

[4M]

